



ADTST.021AUS

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leon Lee Chen et al. ) Group Art Unit 2133  
Appl. No. : 09/839,013 )  
Filed : April 21, 2001 )  
For : SEMICONDUCTOR TEST SYSTEM )  
WITH TIME CRITICAL SEQUENCE )  
GENERATION USING GENERAL )  
PURPOSE OPERATING SYSTEM )  
Examiner : Mujtaba M. Chaudry )

RECEIVED

JUL 13 2004

Technology Center 2100

REQUEST FOR APPROVAL OF DRAWING CHANGES

Hon. Commissioner  
of Patents and Trademarks  
Alexandria, VA 22313-1450

Dear Sir:

This is to request for approval of drawing changes in which a  
"Prior Art" legend is added to Figures 1A-1D and Figures 2A-2D.  
Replacement sheets of the drawing are also submitted herewith.

Respectfully submitted,

MURAMATSU & ASSOCIATES

Dated: 6/21/04

By: *Yasuo Muramatsu*

Yasuo Muramatsu  
Registration No. 38,684  
Attorney of Record  
7700 Irvine Center Drive  
Suite 225, Irvine, CA 92618  
(949) 753-1127